

Download Formal Methods In Systems Engineering

In computer science, specifically software engineering and hardware engineering, formal methods are a particular kind of mathematically based technique for the specification, development and verification of software and hardware systems. The use of formal methods for software and hardware design is motivated by the expectation that, as in other ...Introduction . This document contains some pointers to information on Formal Methods ?, useful for mathematically describing and reasoning about computer-based systems, available around the world on the World Wide Web (WWW).In the context of hardware and software systems, formal verification is the act of proving or disproving the correctness of intended algorithms underlying a system with respect to a certain formal specification or property, using formal methods of mathematics.Objective Developing "safe" systems assuring reliability and correctness by performing researches on the theories of formal logic and formal methods based on them.